



#0104

Sheet 1 of 1

FORM PTO-1449	Atty. Docket No.: 02-4712/L13.12-0228	Appl. No.: 10/664,137
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	First Named Inventor:	
	Jonathan William Byrn et al.	
	Filing Date	Group Art:
	September 17, 2003	2827

2926

## U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						

## FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translation Yes No
AI						

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EP	AJ	Rapid Chip™ Technology, "Fast Custom Silicon through Platform-Based Design" of LSI LOGIC, Copyright 2002, dated April 2003, pages 1-14
EP	AK	Rapid Chip, "platforms," "New family of RapidChip Foundation Slices" of LSI LOGIC, May 1, 2003, website pages 1 and 2
EP	AL	Rapid Chip, "Semiconductor Platform," copyright 2002 by LSI Logic Corporation, 4 pages
EP	AM	Rapid Chip, "platforms," "RapidChip frequently asked questions" of LSI LOGIC, May 1, 2003, website pages 1 and 2
EP	AN	Rapid Chip, "platforms," "Redefining the way chips are made" of LSI LOGIC, May 1, 2003, website pages 1 and 2
EP	AO	Rapid Chip, "Fast facts" of LSI LOGIC, May 1, 2003, website page 1

EXAMINER:

Evan Perz

DATE CONSIDERED:

2/22/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.